

PATENT

2173 #2 8-21-03

Attorney Docket No. CR0234M (72460)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Applicant(s): | Mike Danielson et al. |) |
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| | |) <u>CERTIFICATE OF MAILING</u> |
| Appln No.: | 10/036,910 | I hereby certify that this paper (along with any paper |
| Filed: | December 21, 2001 |) referred to as being attached or enclosed) is being) deposited with the United States Postal Service with sufficient postage as first class mail in an envelope |
| For: | VIDEO SHAPE PADDING | addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this |
| | METHOD |) date. |
| Group Art Unit: | 2173 |) 8/14/03 Date Steven G. Parmelee Registration No. 28,790 |
| • | · · · · · · · · · · · · · · · · · · · | Attorney for Applicant(s) |
| Examiner: | Unassigned | |
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| Commissioner | | RECE/I/P |
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Pursuant to the duty of disclosure under 37 CFR § 1.56, and in accordance with 37 CFR §§ 1.97 and 1.98, Applicants bring the following information with respect to the above-identified application to the attention of the Examiner.

NON PATENT LITERATURE DOCUMENTS

Christoph HEER and Kay MIGGE; "VLSI Hardware accelerator for the MPEG-4 padding algorithm"; Part of the IS&T/SPIE Conference on Media Processors 1999, San Jose, California, January 1999; SPIE Vol. 3655; pgs. 113-119

The above document is listed on Form PTO/SB/08 which accompanies this Information Disclosure Statement, and a copy of each document listed is enclosed herewith.

Pursuant to 37 CFR § 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b).

Respectfully submitted,

Bv:

Steven G. Parmelee Registration No. 28,790

Date: August 14, 2003

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| Application Number | 10/036,910 |
|----------------------|-----------------------|
| Filing Date | 12/21/2001 |
| First Named Inventor | Mike DANIELSON et al. |
| Art Unit | 2173 |
| Examiner Name | Unassigned |
| Attorney Docket | CR0234M (72460) |

| | NON PATENT LITERATURE DOCUMENTS | | | |
|-----------------------|---------------------------------|--|----------------|--|
| Examiner Initials* | Cite No.1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T ² | |
| | | Christoph HEER and Kay MIGGE; "VLSI Hardware accelerator for the MPEG-4 padding algorithm"; Part of the IS&T/SPIE Conference on Media Processors 1999, San Jose, California, January 1999; SPIE Vol. 3655; pgs. 113-119 | | |
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| Examiner Signature | | Date Considered | | |

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional).

Applicant is to place a check mark here if English language Translation is attached.